## **SIEMENS**

### Data sheet

6ES7314-6BH04-0AB0



SIMATIC S7-300, CPU 314C-2 PTP COMPACT CPU WITH MPI, 24 DI/16 DO, 4AI, 2AO, 1 PT100, 4 FAST COUNTERS (60 KHZ), INTEGRATED INTERFACE RS485, INTEGRATED 24V DC POWER SUPPLY, 192 KBYTE WORKING MEMORY, FRONT CONNECTOR (2 X 40PIN) AND MICRO MEMORY CARD REQUIRED

General information	
Hardware product version	01
Firmware version	V3.3
Engineering with	
Programming package	STEP 7 as of V5.5 + SP1 or STEP 7 V5.3 + SP2 or higher with HSP 204
Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines (recommendation)	Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A
Mains buffering	
Mains/voltage failure stored energy time	5 ms
<ul> <li>Repeat rate, min.</li> </ul>	1 s
Digital inputs	
Load voltage L+	
— Rated value (DC)	24 V

Decrees a clarity marketing	Yes
— Reverse polarity protection	165
Digital outputs	
Load voltage L+	24.1/
— Rated value (DC)	24 V
<ul> <li>Reverse polarity protection</li> </ul>	No
Input current	
Current consumption (rated value)	660 mA
Current consumption (in no-load operation), typ.	150 mA
Inrush current, typ.	5 A
l²t	0.7 A <sup>2</sup> ·s
Digital inputs	
<ul><li>from load voltage L+ (without load), max.</li></ul>	80 mA
Digital outputs	
● from load voltage L+, max.	50 mA
Power loss	
Power loss, typ.	13 W
Memory	
Work memory	
• integrated	192 kbyte
• expandable	No
Size of retentive memory for retentive data	64 kbyte
blocks	
Load memory	
• Plug-in (MMC)	Yes
<ul><li>Plug-in (MMC), max.</li></ul>	8 Mbyte
<ul> <li>Data management on MMC (after last</li> </ul>	10 y
programming), min.	
Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
without battery	Yes; Program and data
CPU processing times	
for bit operations, typ.	0.06 µs
for word operations, typ.	0.12 μs
for fixed point arithmetic, typ.	0.16 µs
for floating point arithmetic, typ.	0.59 μs
CPU-blocks	
Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
DB	
Number, max.	1 024; Number range: 1 to 16000
• Size, max.	64 kbyte

FB	
Number, max.	1 024; Number range: 0 to 7999
● Size, max.	64 kbyte
FC	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
OB	
Description	see instruction list
• Size, max.	64 kbyte
<ul> <li>Number of free cycle OBs</li> </ul>	1; OB 1
<ul> <li>Number of time alarm OBs</li> </ul>	1; OB 10
<ul> <li>Number of delay alarm OBs</li> </ul>	2; OB 20, 21
<ul> <li>Number of cyclic interrupt OBs</li> </ul>	4; OB 32, 33, 34, 35
<ul> <li>Number of process alarm OBs</li> </ul>	1; OB 40
<ul> <li>Number of startup OBs</li> </ul>	1; OB 100
<ul> <li>Number of asynchronous error OBs</li> </ul>	4; OB 80, 82, 85, 87
<ul> <li>Number of synchronous error OBs</li> </ul>	2; OB 121, 122
Nesting depth	
per priority class	16
<ul> <li>additional within an error OB</li> </ul>	4
Counters, timers and their retentivity	
S7 counter	
• Number	256
	256
• Number	256 Yes
Number     Retentivity	
<ul><li>Number</li><li>Retentivity</li><li>— adjustable</li></ul>	Yes
<ul><li>Number</li><li>Retentivity</li><li>— adjustable</li><li>— lower limit</li></ul>	Yes 0
<ul> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>— lower limit</li> <li>— upper limit</li> </ul>	Yes 0 255
<ul> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>— lower limit</li> <li>— upper limit</li> <li>— preset</li> </ul>	Yes 0 255
<ul> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>— lower limit</li> <li>— upper limit</li> <li>— preset</li> <li>Counting range</li> </ul>	Yes 0 255 Z 0 to Z 7
● Number  Retentivity  — adjustable — lower limit — upper limit — preset  Counting range — lower limit	Yes 0 255 Z 0 to Z 7
● Number  Retentivity  — adjustable — lower limit — upper limit — preset  Counting range — lower limit — upper limit — upper limit	Yes 0 255 Z 0 to Z 7
● Number  Retentivity  — adjustable — lower limit — upper limit — preset  Counting range — lower limit — upper limit — upper limit	Yes 0 255 Z 0 to Z 7  0 999
<ul> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>— lower limit</li> <li>— upper limit</li> <li>— preset</li> <li>Counting range</li> <li>— lower limit</li> <li>— upper limit</li> <li>IEC counter</li> <li>● present</li> </ul>	Yes 0 255 Z 0 to Z 7  0 999
<ul> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>— lower limit</li> <li>— upper limit</li> <li>— preset</li> <li>Counting range</li> <li>— lower limit</li> <li>— upper limit</li> <li>— upper limit</li> <li>IEC counter</li> <li>● present</li> <li>● Type</li> </ul>	Yes 0 255 Z 0 to Z 7  0 999  Yes SFB Unlimited (limited only by RAM capacity)
● Number  Retentivity  — adjustable — lower limit — upper limit — preset  Counting range — lower limit — upper limit          — upper limit          — upper limit          — Iower limit          — upper limit  IEC counter  ● present ● Type ● Number	Yes 0 255 Z 0 to Z 7  0 999  Yes SFB
<ul> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>— lower limit</li> <li>— upper limit</li> <li>— preset</li> <li>Counting range</li> <li>— lower limit</li> <li>— upper limit</li> <li>IEC counter</li> <li>● present</li> <li>● Type</li> <li>● Number</li> <li>S7 times</li> </ul>	Yes 0 255 Z 0 to Z 7  0 999  Yes SFB Unlimited (limited only by RAM capacity)
● Number  Retentivity  — adjustable — lower limit — upper limit — preset  Counting range — lower limit — upper limit  IEC counter  ● present ● Type ● Number  S7 times ● Number	Yes 0 255 Z 0 to Z 7  0 999  Yes SFB Unlimited (limited only by RAM capacity)  256  Yes
<ul> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>— lower limit</li> <li>— upper limit</li> <li>— preset</li> <li>Counting range</li> <li>— lower limit</li> <li>— upper limit</li> <li>IEC counter</li> <li>● present</li> <li>● Type</li> <li>● Number</li> <li>S7 times</li> <li>● Number</li> <li>Retentivity</li> </ul>	Yes 0 255 Z 0 to Z 7  0 999  Yes SFB Unlimited (limited only by RAM capacity)

— preset	No retentivity
Time range	
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes
• Type	SFB
• Number	Unlimited (limited only by RAM capacity)
Data areas and their retentivity	
retentive data area in total	All, max. 64 KB
Flag	
<ul><li>Number, max.</li></ul>	256 byte
Retentivity available	Yes; MB 0 to MB 255
<ul> <li>Retentivity preset</li> </ul>	MB 0 to MB 15
<ul> <li>Number of clock memories</li> </ul>	8; 1 memory byte
Data blocks	
<ul><li>Number, max.</li></ul>	1 024; Number range: 1 to 16000
• Size, max.	64 kbyte
Retentivity adjustable	Yes; via non-retain property on DB
Retentivity preset	Yes
Local data	
• per priority class, max.	32 kbyte; Max. 2048 bytes per block
Address area	
I/O address area	4 004 h. 4.
• Inputs	1 024 byte
• Outputs	1 024 byte
of which distributed	
— Inputs	none
— Outputs	none
Process image	
• Inputs	1 024 byte
<ul><li>Outputs</li></ul>	1 024 byte
<ul><li>Inputs, adjustable</li></ul>	1 024 byte
<ul> <li>Outputs, adjustable</li> </ul>	1 024 byte
<ul><li>Inputs, default</li></ul>	128 byte
Outputs, default	128 byte
Default addresses of the integrated channels	
— Digital inputs	124.0 to 126.7
— Digital outputs	124.0 to 125.7
— Analog inputs	752 to 761
— Analog outputs	752 to 755

Digital channels	
• Inputs	1 016
— of which central	1 016
Outputs	1 008
— of which central	1 008
Analog channels	
• Inputs	253
— of which central	253
Outputs	250
— of which central	250
— or which certifal	200
Hardware configuration	
Number of expansion units, max.	3
Number of DP masters	
• integrated	none
• via CP	4
Number of operable FMs and CPs (recommended)	
• FM	8
• CP, PtP	8
• CP, LAN	10
Rack	
• Racks, max.	4
<ul> <li>Modules per rack, max.</li> </ul>	8; In rack 3 max. 7
Time of day	
Clock	
<ul> <li>Hardware clock (real-time)</li> </ul>	Yes
<ul> <li>retentive and synchronizable</li> </ul>	Yes
Backup time	6 wk; At 40 °C ambient temperature
<ul><li>Deviation per day, max.</li></ul>	10 s; Typ.: 2 s
<ul> <li>Behavior of the clock following POWER-ON</li> </ul>	Clock continues running after POWER OFF
<ul> <li>Behavior of the clock following expiry of backup period</li> </ul>	Clock continues to run with the time at which the power failure occurred
Operating hours counter	
• Number	1
Number/Number range	0
Range of values	0 to 2^31 hours (when using SFC 101)
Granularity	1 hour
• retentive	Yes; Must be restarted at each restart
Clock synchronization	
• supported	Yes
• to MPI, master	Yes
● to MPI, slave	Yes

• in AS, master	Yes
• in AS, slave	No
Digital inputs	
Number of digital inputs	24
<ul> <li>of which inputs usable for technological</li> </ul>	16
functions	
integrated channels (DI)	24
Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	24
— up to 60 °C, max.	12
vertical installation	
— up to 40 °C, max.	12
Input voltage	
Rated value (DC)	24 V
● for signal "0"	-3 to +5V
● for signal "1"	+15 to +30V
Input current	
• for signal "1", typ.	8 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.)
— Rated value	3 ms
for counter/technological functions	
— at "0" to "1", max.	8 μs; Minimum pulse width/minimum pause between pulses at maximum counting frequency
Cable length	
• shielded, max.	1 000 m; 50 m for technological functions
• unshielded, max.	600 m; For technological functions: No
for technological functions	
— shielded, max.	50 m; at maximum count frequency
— unshielded, max.	not allowed
Digital outputs	
Number of digital outputs	16
• of which high-speed outputs	4; Notice: You cannot connect the fast outputs of your CPU in parallel
integrated channels (DO)	16

Short-circuit protection	Yes; Clocked electronically
Response threshold, typ.	1 A
Limitation of inductive shutdown voltage to	L+ (-48 V)
Controlling a digital input	Yes
Switching capacity of the outputs	
● on lamp load, max.	5 W
Load resistance range	
• lower limit	48 Ω
• upper limit	4 kΩ
Output voltage	
● for signal "1", min.	L+ (-0.8 V)
Output current	
● for signal "1" rated value	500 mA
• for signal "1" permissible range, min.	5 mA
• for signal "1" permissible range, max.	0.6 A
for signal "1" minimum load current	5 mA
• for signal "0" residual current, max.	0.5 mA
Parallel switching of two outputs	
• for uprating	No
• for redundant control of a load	Yes
Switching frequency	
with resistive load, max.	100 Hz
with inductive load, max.	0.5 Hz
● on lamp load, max.	100 Hz
• of the pulse outputs, with resistive load, max.	2.5 kHz
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	3 A
— up to 60 °C, max.	2 A
vertical installation	
— up to 40 °C, max.	2 A
Cable length	
• shielded, max.	1 000 m
• unshielded, max.	600 m
Analog inputs  Number of analog inputs	5
	4
For voltage/current measurement	1
<ul> <li>For resistance/resistance thermometer measurement</li> </ul>	'
integrated channels (AI)	5; 4 x current/voltage, 1 x resistance
permissible input voltage for current input (destruction limit), max.	5 V; Permanent

permissible input voltage for voltage input (destruction limit), max.	30 V; Permanent
permissible input current for voltage input (destruction limit), max.	0.5 mA; Permanent
permissible input current for current input (destruction limit), max.	50 mA; Permanent
Technical unit for temperature measurement adjustable	Yes; Degrees Celsius / degrees Fahrenheit / Kelvin
Input ranges	
Voltage	Yes; $\pm 10$ V / $100$ k $\Omega$ ; 0 V to 10 V / $100$ k $\Omega$
• Current	Yes; ±20 mA / 100 $\Omega$ ; 0 mA to 20 mA / 100 $\Omega$ ; 4 mA to 20 mA / 100 $\Omega$
<ul> <li>Resistance thermometer</li> </ul>	Yes; Pt 100 / 10 MΩ
Resistance	Yes; 0 $\Omega$ to 600 $\Omega$ / 10 M $\Omega$
Input ranges (rated values), voltages	
• 0 to +10 V	Yes
<ul><li>Input resistance (0 to 10 V)</li></ul>	100 kΩ
Input ranges (rated values), currents	
• 0 to 20 mA	Yes
<ul><li>Input resistance (0 to 20 mA)</li></ul>	100 Ω
• -20 mA to +20 mA	Yes
<ul> <li>Input resistance (-20 mA to +20 mA)</li> </ul>	100 Ω
• 4 mA to 20 mA	Yes
<ul> <li>Input resistance (4 mA to 20 mA)</li> </ul>	100 Ω
Input ranges (rated values), resistance thermometer	
• Pt 100	Yes
<ul><li>Input resistance (Pt 100)</li></ul>	10 MΩ
Input ranges (rated values), resistors	
No-load voltage, typ.	3.3 V
<ul> <li>Measuring current, typ.</li> </ul>	1,25 mA
• 0 to 600 ohms	Yes
<ul> <li>Input resistance (0 to 600 ohms)</li> </ul>	10 MΩ
Thermocouple (TC)	
Temperature compensation	
— parameterizable	No
Characteristic linearization	
parameterizable	Yes; by software
— for resistance thermometer	Pt 100
Cable length	
• shielded, max.	100 m
Analog outputs	
Number of analog outputs	2

integrated channels (AO)	2
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit protection  Voltage output, short-circuit current, max.	55 mA
Current output, no-load voltage, max.	14 V
Output ranges, voltage	17 V
• 0 to 10 V	Yes
• -10 V to +10 V	Yes
	165
Output ranges, current	Yes
• 0 to 20 mA	Yes
• -20 mA to +20 mA	
• 4 mA to 20 mA	Yes
Connection of actuators	
<ul> <li>for voltage output two-wire connection</li> </ul>	Yes; Without compensation of the line resistances
<ul> <li>for voltage output four-wire connection</li> </ul>	No
<ul> <li>for current output two-wire connection</li> </ul>	Yes
Load impedance (in rated range of output)	
<ul><li>with voltage outputs, min.</li></ul>	1 kΩ
<ul> <li>with voltage outputs, capacitive load, max.</li> </ul>	0.1 μF
<ul><li>with current outputs, max.</li></ul>	300 Ω
<ul> <li>with current outputs, inductive load, max.</li> </ul>	0.1 mH
Destruction limits against externally applied voltages an	d currents
<ul> <li>Voltages at the outputs towards MANA</li> </ul>	16 V; Permanent
• Current, max.	50 mA; Permanent
• Current, max.  Cable length	50 mA; Permanent
	50 mA; Permanent 200 m
Cable length	
Cable length  ● shielded, max.	
Cable length  ● shielded, max.  Analog value generation for the inputs	200 m
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle	200 m
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign),	200 m  Actual value encryption (successive approximation)
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.	200 m  Actual value encryption (successive approximation)  12 bit
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for	200 m  Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz	200 m  Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms 50 / 60 Hz
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz  • permissible input frequency, max.	200 m  Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms 50 / 60 Hz  400 Hz
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz  • permissible input frequency, max.  • Time constant of the input filter  • Basic execution time of the module (all	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms 50 / 60 Hz  400 Hz 0.38 ms
Cable length  • shielded, max.  Analog value generation for the inputs  Measurement principle  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Interference voltage suppression for interference frequency f1 in Hz  • permissible input frequency, max.  • Time constant of the input filter  • Basic execution time of the module (all channels released)	Actual value encryption (successive approximation)  12 bit  Yes; 16.6 / 20 ms 50 / 60 Hz  400 Hz 0.38 ms

• Conversion time (per channel)

max.

1 ms

# Settling time • for resistive load • for capacitive load • for inductive load • for inductive load 0.6 ms 1 ms 0.5 ms

• for capacitive load	1110
• for inductive load	0.5 ms
Encoder	
Connection of signal encoders	
for voltage measurement	Yes
• for current measurement as 2-wire transducer	Yes; with external supply
• for current measurement as 4-wire transducer	Yes
<ul> <li>for resistance measurement with two-wire connection</li> </ul>	Yes; Without compensation of the line resistances
<ul> <li>for resistance measurement with three-wire connection</li> </ul>	No
<ul> <li>for resistance measurement with four-wire connection</li> </ul>	No
Connectable encoders	
• 2-wire sensor	Yes
<ul> <li>permissible quiescent current (2-wire sensor), max.</li> </ul>	1.5 mA
Errors/accuracies	

Temperature error (relative to input range), (+/-)	0.006 %/K
Crosstalk between the inputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to	0.06 %
input range), (+/-)	
Output ripple (relative to output range, bandwidth 0 to	0.1 %
50 kHz), (+/-)	
Linearity error (relative to output range), (+/-)	0.15 %
Temperature error (relative to output range), (+/-)	0.01 %/K
Crosstalk between the outputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to	0.06 %
output range), (+/-)	
Operational error limit in overall temperature range	
<ul> <li>Voltage, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Current, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Resistance, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Voltage, relative to output range, (+/-)</li> </ul>	1 %
<ul> <li>Current, relative to output range, (+/-)</li> </ul>	1 %
Basic error limit (operational limit at 25 °C)	
<ul> <li>Voltage, relative to input range, (+/-)</li> </ul>	0.8 %; Linearity error +/- 0.06 %
<ul> <li>Current, relative to input range, (+/-)</li> </ul>	0.8 %; Linearity error +/- 0.06 %
<ul> <li>Resistance, relative to input range, (+/-)</li> </ul>	0.8 %; Linearity error +/- 0.2%
<ul> <li>Resistance thermometer, relative to input</li> </ul>	0.8 %
range, (+/-)	

<ul> <li>Voltage, relative to output range, (+/-)</li> </ul>	0.8 %
• Current, relative to output range, (+/-)	0.8 %

### Interference voltage suppression for f = n x (f1 +/- 1 %), f1 = interference frequency

• Series mode interference (peak value of interference < rated value of input range), min.

30 dB

• Common mode interference, min.

40 dB

Interfaces	
Number of industrial Ethernet interfaces	0
Number of RS 485 interfaces	1; MPI
Number of RS 422 interfaces	1; RS 422/485 combined
Point-to-point	
Cable length, max.	1 200 m
Integrated protocol driver	
— 3964 (R)	Yes
— ASCII	Yes
— RK512	Yes
Transmission rate, RS 422/485	
— with 3964 (R) protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with ASCII protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with RK 512 protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex

1. Interface	
Interface type	Integrated RS 485 interface
Physics	RS 485
Isolated	No
Power supply to interface (15 to 30 V DC), max.	200 mA
Functionality	
● MPI	Yes
<ul> <li>PROFIBUS DP master</li> </ul>	No
<ul> <li>PROFIBUS DP slave</li> </ul>	No
Point-to-point connection	No
MPI	
Transmission rate, max.	187.5 kbit/s
Services	
<ul><li>— PG/OP communication</li></ul>	Yes
— Routing	No
<ul> <li>Global data communication</li> </ul>	Yes
<ul> <li>S7 basic communication</li> </ul>	Yes
— S7 communication	Yes; Only server, configured on one side
<ul> <li>S7 communication, as client</li> </ul>	No; but via CP and loadable FB
— S7 communication, as server	Yes

#### 2. Interface

Interface type	Integrated RS 422/ 485 interface
Physics	RS 422/RS 485 (X.27)
Isolated	Yes
Power supply to interface (15 to 30 V DC), max.	No
Functionality	
• MPI	No
<ul> <li>PROFINET IO Controller</li> </ul>	No
PROFINET IO Device	No
PROFINET CBA	No
PROFIBUS DP master	No
PROFIBUS DP slave	No
Point-to-point connection	Yes
Point-to-point connection	
Transmission rate, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
• Interface controllable from the user program	Yes
<ul> <li>Interface can trigger alarm/interrupt in the user</li> </ul>	Yes; Message on break - identification
program	
Communication functions	
PG/OP communication	Yes
Data record routing	No
Global data communication	
• supported	Yes
<ul> <li>Number of GD loops, max.</li> </ul>	8
<ul> <li>Number of GD packets, max.</li> </ul>	8
Number of GD packets, transmitter, max.	8
<ul> <li>Number of GD packets, receiver, max.</li> </ul>	8
Size of GD packets, max.	22 byte
Size of GD packet (of which consistent), max.	22 byte
S7 basic communication	
• supported	Yes
<ul> <li>User data per job, max.</li> </ul>	76 byte
• User data per job (of which consistent), max.	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
User data per job, max.	180 kbyte; With PUT/GET
• User data per job (of which consistent), max.	240 byte; as server
S5 compatible communication	
• supported	Yes; via CP and loadable FC
Number of connections	

• overall	12
<ul> <li>usable for PG communication</li> </ul>	11
<ul> <li>reserved for PG communication</li> </ul>	1
— adjustable for PG communication, min.	1
— adjustable for PG communication, max.	11
<ul> <li>usable for OP communication</li> </ul>	11
<ul> <li>reserved for OP communication</li> </ul>	1
— adjustable for OP communication, min.	1
— adjustable for OP communication, max.	11
<ul> <li>usable for S7 basic communication</li> </ul>	8
<ul> <li>reserved for S7 basic communication</li> </ul>	0
<ul> <li>adjustable for S7 basic communication,</li> </ul>	0
min.	
<ul> <li>adjustable for S7 basic communication,</li> </ul>	8
max.	

S7 message functions	
Number of login stations for message functions, max.	12; Depending on the configured connections for PG/OP and S7
	basic communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	300

Test commissioning functions	
Status block	Yes; Up to 2 simultaneously
Single step	Yes
Number of breakpoints	4
Status/control	
Status/control variable	Yes
<ul> <li>Variables</li> </ul>	Inputs, outputs, memory bits, DB, times, counters
<ul> <li>Number of variables, max.</li> </ul>	30
<ul><li>of which status variables, max.</li></ul>	30
— of which control variables, max.	14
Forcing	
<ul><li>Forcing</li></ul>	Yes
<ul> <li>Forcing, variables</li> </ul>	Inputs, outputs
<ul> <li>Number of variables, max.</li> </ul>	10
Diagnostic buffer	
• present	Yes
<ul> <li>Number of entries, max.</li> </ul>	500
— adjustable	No
— of which powerfail-proof	100; Only the last 100 entries are retained
<ul> <li>Number of entries readable in RUN, max.</li> </ul>	499
— can be set	Yes; From 10 to 499

Foreign and sparation  Potential separation digital inputs  Potential separation digital inputs  Potential separation digital outputs  Potential separation digital outputs  Potential separation digital outputs  Potential separation digital outputs  Potential separation analog inputs  Potential separation analog outputs  Potential separation a		10
Can be read out   Yes	— preset	10
Diagnostics indicator LED  Status indicator digital input (green) Status indicator digital output (green)  Ves  Integrated Functions Number of counters Superior (counter) max. Frequency measurement Superior (green) was superior (green) superior (green)		Voc
Diagnostics indicator digital input (green)  • Status indicator digital output (green)  • Status indicator digital output (green)  • Status indicator digital output (green)  * Status indicator digital input (green)  * Potential separation  * Potential separation digital inputs  * Potential separation digital inputs  * Potential separation digital inputs  * Potential separation digital outputs  * Potential separation analog inputs  * Potential separation analog outputs  * Potential s	• can be read out	165
Status indicator digital input (green) Status indicator digital output (green) Status indicator digital output (green) Yes  Integrated Functions  Number of counters Counting frequency (counter) max. Frequency measurement Yes Number of frequency meters 4; up to 60 kHz (see "Technological Functions" manual) Yes Ves Number of frequency meters 4; up to 60 kHz (see "Technological Functions" manual) Yes Ves Number of peutonion blocks (closed-loop control) Yes: PID controller (see "Technological Functions" manual) Yes Number of pulse outputs 4; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual) Limit frequency (pulse)  Potential separation Potential separation Potential separation digital inputs • Potential separation digital inputs • between the channels • between the channels and backplane bus Potential separation digital outputs • between the channels • between the channels and backplane bus Potential separation analog inputs • between the channels and backplane bus Potential separation analog inputs • between the channels and backplane bus Potential separation analog inputs • between the channels and backplane bus Potential separation analog outputs • between the channels and backplane bus Potential separation analog outputs • between the channels and backplane bus Potential separation analog outputs • between the channels and backplane bus Potential separation analog outputs • between the channels • between the inputs and MANA (UCM)  8 V DC	Interrupts/diagnostics/status information	
Status indicator digital output (green)  Number of counters  Counting frequency (counter) max.  Frequency measurement  Number of frequency meters  Controlled positioning  Integrated function blocks (closed-loop control)  PID controller  Number of pulse outputs  Ves  Number of pulse outputs  Ves  Number of pulse outputs  Limit frequency (pulse)  Potential separation digital inputs  Potential separation digital outputs  Potential separation analog inputs  Potential separation analog outputs  Potential separation analo	Diagnostics indication LED	
Integrated Functions  Number of counters  Counting frequency (counter) max.  Frequency measurement  Number of frequency meters  Ountrolled positioning  Integrated function blocks (closed-loop control)  PIC controller  Number of pulse outputs  Ves  Number of pulse outputs  Potential separation  Potential separation digital inputs  Potential separation digital outputs  Potential separation analog inputs  Potential separation analog outputs  P	<ul> <li>Status indicator digital input (green)</li> </ul>	Yes
Number of counters  Counting frequency (counter) max. Frequency measurement  Yes  Number of frequency meters  controlled positioning  integrated function blocks (closed-loop control)  PID controller  Number of pulse outputs  Limit frequency (pulse)  Potential separation digital inputs  Potential separation digital outputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog outputs  Potential	<ul> <li>Status indicator digital output (green)</li> </ul>	Yes
Number of counters  Counting frequency (counter) max. Frequency measurement  Yes  Number of frequency meters  controlled positioning  integrated function blocks (closed-loop control)  PID controller  Number of pulse outputs  Limit frequency (pulse)  Potential separation digital inputs  Potential separation digital outputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog outputs  Potential	Integrated Functions	
Frequency measurement Number of frequency meters A; up to 60 kHz (see "Technological Functions" manual) Controlled positioning Integrated function blocks (closed-loop control) PID controller Yes Number of pulse outputs A; Pulse width modulation up to 2.5 kHz (see "Technological Functions" manual) Potential separation Potential separation digital inputs Potential separation digital inputs Potential separation digital inputs between the channels between the channels between the channels Potential separation digital outputs Potential separation analog inputs Potential separation analog inputs Potential separation analog inputs Potential separation analog outputs Potential difference Between the inputs and MANA (UCM)  B V DC	Number of counters	4; See "Technological Functions" manual
Number of frequency meters  controlled positioning  integrated function blocks (closed-loop control)  PID controller  Yes  Number of pulse outputs  Limit frequency (pulse)  Potential separation  Potential separation digital inputs  • between the channels and backplane bus  • between the channels and backplane bus  Potential separation analog inputs  • between the channels and backplane bus  Potential separation analog outputs  Potential separation analog outputs  • Potential separation analog outputs  • Potential separation analog outputs  • Potential separation and digital inputs  • between the channels yes  • between the channels and backplane bus  Potential separation analog inputs  • Potential separation analog inputs  • Potential separation analog outputs  • Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	Counting frequency (counter) max.	60 kHz
controlled positioning integrated function blocks (closed-loop control) PID controller Number of pulse outputs  Limit frequency (pulse)  Potential separation Potential separation digital inputs  between the channels between the channels, in groups of between the channels, in groups of between the channels and backplane bus Potential separation analog inputs  Potential separation analog outputs  Potential separation analog outputs  Potential separation digital outputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog outputs  Potential separ	Frequency measurement	Yes
integrated function blocks (closed-loop control)  PID controller  Number of pulse outputs  4; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)  Limit frequency (pulse)  Potential separation  Potential separation digital inputs  • Potential separation digital inputs  • between the channels  • Potential separation digital outputs  • Potential separation and backplane bus  Potential separation analog inputs  • Potential separation analog inputs  • Potential separation analog inputs  • Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels  • between th	Number of frequency meters	4; up to 60 kHz (see "Technological Functions" manual)
PID controller  Number of pulse outputs  4; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)  Limit frequency (pulse)  2.5 kHz  Potential separation  Potential separation digital inputs  • Potential separation digital inputs  • between the channels and backplane bus  Potential separation digital outputs  • between the channels  • between the channels in groups of  • between the channels and backplane bus  Potential separation analog inputs  • Potential separation analog inputs  • Potential separation analog inputs  • between the channels in groups of the search of	controlled positioning	Yes
Number of pulse outputs  4; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)  Limit frequency (pulse)  2.5 kHz   Potential separation  Potential separation digital inputs  • Potential separation digital inputs  • between the channels  • between the channels and backplane bus  Potential separation digital outputs  • Potential separation digital outputs  • Potential separation digital outputs  • between the channels  • between the channels yes  • between the channels in groups of  • between the channels and backplane bus  Potential separation analog inputs  • Potential separation analog inputs  • Potential separation analog inputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • Potential separation analog outputs  • between the channels  • Detential separation analog outputs  • between the channels  • between the channels  • between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	integrated function blocks (closed-loop control)	Yes; PID controller (see "Technological Functions" manual)
Functions" Manual)  Limit frequency (pulse)  2.5 kHz   Potential separation  Potential separation digital inputs  • Potential separation digital inputs  • between the channels  • between the channels and backplane bus  Potential separation digital outputs  • between the channels  • between the channels, in groups of  • between the channels and backplane bus  Potential separation analog inputs  • Potential separation analog inputs  • Potential separation analog inputs  • Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • Potential separation analog outputs  • Potential separation analog outputs  • between the channels  • between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	PID controller	Yes
Potential separation Potential separation digital inputs  • Potential separation digital inputs • between the channels • between the channels and backplane bus  Potential separation digital outputs  • Potential separation digital outputs  • Potential separation digital outputs  • between the channels • between the channels, in groups of • between the channels and backplane bus  Potential separation analog inputs  • between the channels • between the channels and backplane bus  Potential separation analog outputs  • between the channels and backplane bus  Potential separation analog outputs  • between the channels • between the channels • between the channels • between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	Number of pulse outputs	
Potential separation digital inputs  Potential separation digital inputs  between the channels  Potential separation digital outputs  between the channels  between the channels, in groups of  between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  between the channels  Potential separation analog outputs  Potential separa	Limit frequency (pulse)	2.5 kHz
Potential separation digital inputs  Potential separation digital inputs  between the channels  Potential separation digital outputs  between the channels  between the channels, in groups of  between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  between the channels  Potential separation analog outputs  Potential separa	Potential separation	
• between the channels     • between the channels and backplane bus  Potential separation digital outputs  Potential separation digital outputs  Potential separation digital outputs  between the channels  between the channels, in groups of  between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  between the channels  between the channels  between the channels and backplane bus  Potential separation analog outputs  Potential sepa		
between the channels and backplane bus  Potential separation digital outputs  Potential separation digital outputs  Potential separation digital outputs  between the channels  between the channels, in groups of  between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  between the channels  between the channels and backplane bus  Potential separation analog outputs  Potential deparation analog outputs  Potential separation analog	Potential separation digital inputs	Yes
Potential separation digital outputs  Potential separation digital outputs  between the channels  between the channels, in groups of  between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog outputs  between the channels and backplane bus  Potential separation analog outputs  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	• between the channels	No
Potential separation digital outputs  between the channels  between the channels, in groups of  between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  between the channels  between the channels and backplane bus  Potential separation analog outputs  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	<ul> <li>between the channels and backplane bus</li> </ul>	Yes
between the channels     between the channels, in groups of     between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  between the channels  between the channels  Potential separation analog outputs  Potential	Potential separation digital outputs	
between the channels, in groups of     between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  Potential separation analog inputs  between the channels  between the channels and backplane bus  Potential separation analog outputs  Permissible potential difference  Between the inputs and MANA (UCM)  B V DC	Potential separation digital outputs	Yes
between the channels and backplane bus  Potential separation analog inputs  Potential separation analog inputs  between the channels  between the channels  between the channels and backplane bus  Potential separation analog outputs  Permissible potential difference  Between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  Both separation analog inputs  Yes; common for analog I/O  No  Yes; common for analog I/O  No  Separation analog outputs  Permissible potential difference  Between the inputs and MANA (UCM)  Both separation analog inputs  Yes; common for analog I/O  No  No  Yes; common for analog I/O  No  No  Yes; common for analog I/O	• between the channels	Yes
Potential separation analog inputs  Potential separation analog inputs  between the channels  between the channels and backplane bus  Potential separation analog outputs  Potential separation analog outputs  Potential separation analog outputs  Potential separation analog outputs  between the channels  between the channels  between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	<ul> <li>between the channels, in groups of</li> </ul>	8
Potential separation analog inputs between the channels between the channels and backplane bus  Potential separation analog outputs  Petween the channels between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  Between the inputs and MANA (UCM)  Between the inputs and MANA (UCM)	<ul> <li>between the channels and backplane bus</li> </ul>	Yes
between the channels     between the channels and backplane bus  Potential separation analog outputs  Potential separation analog outputs  Potential separation analog outputs  Potential separation analog outputs  No  between the channels  between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC	Potential separation analog inputs	
between the channels and backplane bus  Potential separation analog outputs  Potential separation analog outputs  Potential separation analog outputs  between the channels  between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)	Potential separation analog inputs	Yes; common for analog I/O
between the channels and backplane bus  Potential separation analog outputs  Potential separation analog outputs  Potential separation analog outputs  between the channels  between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC  Isolation		No
Potential separation analog outputs  • Potential separation analog outputs  • between the channels  • between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC		Yes
Potential separation analog outputs     between the channels     between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  Solution  Yes; common for analog I/O  No  Yes  Yes  Yes  Yes  Yes	·	
between the channels     between the channels and backplane bus  Permissible potential difference  Between the inputs and MANA (UCM)  8 V DC  Isolation		Yes; common for analog I/O
between the channels and backplane bus  Yes  Permissible potential difference  Between the inputs and MANA (UCM)  Isolation  Yes  Yes		
Between the inputs and MANA (UCM) 8 V DC  Isolation		Yes
Between the inputs and MANA (UCM) 8 V DC  Isolation	Permissible potential difference	
	<u>_</u>	8 V DC
	Isolation	
		600 V DC

Ambient conditions	
Ambient temperature during operation	
• min.	0 °C
• max.	60 °C
Configuration	
Configuration software	Very CTED 7 VE E + CD4 or higher or CTED 7 VE 2 + CD2 or
• STEP 7	Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
• STEP 7 Lite	No
Programming	
Command set	see instruction list
<ul> <li>Nesting levels</li> </ul>	8
<ul><li>System functions (SFC)</li></ul>	see instruction list
<ul> <li>System function blocks (SFB)</li> </ul>	see instruction list
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
— SCL	Yes
— CFC	Yes
— GRAPH	Yes
— HiGraph®	Yes
Know-how protection	
User program protection/password protection	Yes
Block encryption	Yes; With S7 block Privacy
Dimensions	
Width	120 mm
Height	125 mm
Depth	130 mm
Weights	
Weight, approx.	680 g
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