## **SIEMENS**

## Data sheet

## 6ES7317-6TK13-0AB0

\*\*\* SPARE PART\*\*\* SIMATIC S7-300, CPU 317T-2 DP, CENTRAL PROCESSING UNIT FOR PLC AND TECHNOLOGY 1024 KBYTE WORKING MEMORY, 1. INTERFACE MPI/DP 12MBIT/S 2. INTERFACE DP(DRIVE), INTEGRATED I/O FOR TECHNOLOGY FRONT CONNECTOR (1 X 40PIN) AND MICRO MEMORY CARD MIN. 8 MB NECESSARY



General information	
Hardware product version	01
Firmware version	CPU: V2.7, integrated technology: V4.1.5
Engineering with	
Programming package	STEP 7 V5.4 + SP5 (and higher) and Optional package S7- Technology V4.2
Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
permissible range, lower limit (DC)	20.4 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines (recommendation)	2 A min.
Load voltage L+	
Rated value (DC)	24 V
<ul> <li>Reverse polarity protection</li> </ul>	Yes
Digital outputs	
Load voltage L+	
— Rated value (DC)	24 V; (2L+)

— Reverse polarity protection	No; (2L+)
Input current	
Current consumption (in no-load operation), typ.	200 mA
Inrush current, typ.	2.5 A
l²t	1 A²⋅s
Power loss	
Power loss, typ.	6 W
Memory	
Work memory	
• integrated	1 024 kbyte
• expandable	No
Load memory	
• Plug-in (MMC)	Yes
<ul><li>Plug-in (MMC), max.</li></ul>	8 Mbyte
<ul> <li>Data management on MMC (after last programming), min.</li> </ul>	10 y
Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
• without battery	Yes; Program and data
CPU processing times	
for bit operations, typ.	0.05 μs
for bit operations, max.	0.05 μs
for word operations, typ.	0.2 μs
for fixed point arithmetic, typ.	0.2 μs
for floating point arithmetic, typ.	1 μs
CPU-blocks	
Number of blocks (total)	2 048; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
DB	
<ul><li>Number, max.</li></ul>	2 047; Number band: 1 to 2047
• Size, max.	64 kbyte
FB	
Number, max.	2 048; Number range: 0 to 2047
• Size, max.	64 kbyte
FC	
Number, max.	2 048; Number range: 0 to 2047
• Size, max.	64 kbyte
OB	
Description	see instruction list
• Size, max.	64 kbyte
<ul> <li>Number of free cycle OBs</li> </ul>	1; OB 1

<ul> <li>Number of time alarm OBs</li> </ul>	1; OB 10
<ul> <li>Number of delay alarm OBs</li> </ul>	2; OB 20, 21
<ul> <li>Number of cyclic interrupt OBs</li> </ul>	4; OB 32, 33, 34, 35
<ul> <li>Number of process alarm OBs</li> </ul>	1; OB 40
<ul> <li>Number of DPV1 alarm OBs</li> </ul>	3; OB 55, 56, 57
<ul> <li>Number of isochronous mode OBs</li> </ul>	1; OB 61
<ul> <li>Number of technology synchronous alarm OBs</li> </ul>	1; OB 65
<ul> <li>Number of startup OBs</li> </ul>	1; OB 100
<ul> <li>Number of asynchronous error OBs</li> </ul>	5; OB 80, 82, 85, 86, 87
<ul> <li>Number of synchronous error OBs</li> </ul>	2; OB 121, 122
Nesting depth	
per priority class	16
<ul> <li>additional within an error OB</li> </ul>	4

Counters, timers and their retentivity	
S7 counter	
• Number	512; Number range: 0 to 511
Retentivity	
— adjustable	Yes
— preset	8
Counting range	
— can be set	Yes
— lower limit	0
— upper limit	999
IEC counter	
• present	Yes
• Type	SFB
• Number	Unlimited (limited only by RAM capacity)
S7 times	
Number	512; Number range: 0 to 511
of which retentive without battery	
— adjustable	Yes
Retentivity	
— adjustable	Yes
— preset	No retentivity
Time range	
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes
● Type	SFB
• Number	Unlimited (limited only by RAM capacity)

Telentive data area in total	Data areas and their retentivity	
Number, max.     Retentivity available     Retentivity preset     Number of clock memories     Number of subprocess images     Number of with central     Outputs, default     Outputs     Outputs     Outputs     Outputs     Outputs     Outputs     Outputs     Outputs     Outputs, default     Outputs, default     Outputs     Outputs     Outputs     Outputs, default     Outputs     Outputs     Outputs     Outputs     Outputs     Outputs     Outputs, default     Outputs, default     Outputs		All DBs, max. 256 KB
Retentivity available     Retentivity preset     Retentivity preset     Namber of clock memories     Number of clock memories     Number, max.     Size, max.     Retentivity adjustable     Retentivity adjustable     Retentivity preset     Retentivity adjustable     Per priority class, max.     Retentivity preset     Retentivity adjustable     Retentivity preset     Retentivity and present present present present property on DB     Retentivity preset     Retentivity and present present property on DB     Retentivity preset     Retentivity preset     Retentivity adjustable     Retentivity alloused present pres	Flag	
Retentivity preset Number of clock memories 8; 1 memory byte  Data blocks  Number, max. 2 047; from DB 1 to DB 2047 Size, max. 64 k byte Retentivity adjustable Retentivity preset Yes: via non-retain property on DB Retentivity preset  Local data per priority class, max. 1 024 byte  Address area  No address area I/O address area I/O address area I/O address area  I/O add	Number, max.	4 096 byte
Number of clock memories  Pata blocks  Number, max. Size, max. Size, max. Retentivity adjustable Retentivity preset  Local data Per priority class, max. Inputs Inputs Cutputs Process image Inputs, adjustable Cutputs, default Cutputs, default Default addresses of the integrated channels Digital channels  Digital channels Inputs Size Size Size Size Size Size Size Size	Retentivity available	Yes; From MB 0 to MB 4095
Data blocks	Retentivity preset	MB 0 to MB 15
Number, max.     Size, max.     Retentivity adjustable     Retentivity preset     Yes     Retentivity preset     Yes     Retentivity preset     Yes     Yes     Ves	<ul> <li>Number of clock memories</li> </ul>	8; 1 memory byte
• Size, max.  • Retentivity adjustable  • Retentivity preset  Pes  **Local data**  • per priority class, max.  **Address area**  **Inputs  • Outputs  **Outputs  **Inputs  • Inputs, adjustable  • Outputs, adjustable  • Outputs, default  • Outputs, default  • Default addresses of the integrated channels  — Digital inputs — Digital channels  • Inputs — Outputs  **Outputs  **Outputs  **Outputs  **Outputs  **Outputs  **Outputs, default  • Outputs, default • Outputs • 66  **Subprocess images • Number of subprocess images, max.  1  **Digital channels • Inputs — of which central • Outputs • 65 536 — of which central • 112  **Analog channels • Inputs — of which central • Outputs	Data blocks	
Retentivity adjustable Retentivity preset  Retentivity preset  Yes  Local data  per priority class, max.  1 024 byte  Address area  I/O a	Number, max.	2 047; from DB 1 to DB 2047
Retentivity preset     Local data	• Size, max.	64 kbyte
Local data   • per priority class, max.	<ul> <li>Retentivity adjustable</li> </ul>	Yes; via non-retain property on DB
Per priority class, max.  Address area  I/O	<ul> <li>Retentivity preset</li> </ul>	Yes
Address area	Local data	
Inputs	• per priority class, max.	1 024 byte
Inputs	Address area	
Outputs     of which distributed         — Inputs         — Outputs         — Outputs         — Outputs         — Outputs  Process image          • Inputs, adjustable         • Outputs, adjustable         • Outputs, adjustable         • Outputs, adjustable         • Inputs, default         • Outputs, default         • Outputs, default         • Outputs, default         • Digital inputs         — Digital inputs         — Digital outputs         • 66  Subprocess images          • Number of subprocess images, max.  Digital channels          • Inputs         — of which central         • Outputs         — of which central         • Inputs         — of which central         • Outputs         — Outputs         — of which central         • Outputs		
of which distributed  — Inputs — Outputs  8 192 byte  Process image  • Inputs, adjustable • Outputs, adjustable • Outputs, default • Default addresses of the integrated channels — Digital inputs — Digital outputs  66  Subprocess images • Number of subprocess images, max.  1  Digital channels • Inputs — of which central • Outputs — of which central  • Inputs — of which central  • Outputs — of which central • Outputs — of which central • Outputs — of which central • Outputs — of which central • Outputs — of which central • Outputs — of which central • Outputs — of which central	• Inputs	8 192 byte
Inputs Outputs Outputs Outputs Outputs Outputs Outputs Outputs, adjustable Inputs, adjustable Outputs, adjustable Outputs, default Outputs, default Outputs, default Outputs, default Digital inputs Digital inputs Digital outputs Digital outputs	<ul><li>Outputs</li></ul>	8 192 byte
— Outputs       8 192 byte         Process image       ■ Inputs, adjustable       2 048 byte         ● Outputs, adjustable       2 048 byte         ● Inputs, default       256 byte         ● Outputs, default       256 byte         Default addresses of the integrated channels       — Digital inputs       66         — Digital outputs       66         Subprocess images       ● Number of subprocess images, max.       1         Digital channels       ■ Inputs       65 536         — of which central       512         ● Outputs       65 536         — of which central       512         Analog channels       ■ Inputs       4 096         — of which central       64         ● Outputs       4 096         ● Outputs       4 096	of which distributed	
Process image	— Inputs	8 192 byte
<ul> <li>Inputs, adjustable</li> <li>Outputs, adjustable</li> <li>Outputs, default</li> <li>Outputs, default</li> <li>Outputs, default</li> <li>Outputs, default</li> <li>Default addresses of the integrated channels</li> <li>— Digital inputs</li> <li>— Digital outputs</li> <li>G6</li> <li>Subprocess images</li> <li>Number of subprocess images, max.</li> <li>Digital channels</li> <li>Inputs</li> <li>— of which central</li> <li>Outputs</li> <li>— of which central</li> <li>512</li> <li>Analog channels</li> <li>Inputs</li> <li>— of which central</li> <li>512</li> <li>Analog channels</li> <li>Inputs</li> <li>Of which central</li> <li>Outputs</li> <li>Outputs</li> <li>— of which central</li> <li>Outputs</li> <li>4 096</li> <li>— of which central</li> <li>Outputs</li> <li>4 096</li> </ul>	— Outputs	8 192 byte
Outputs, adjustable     Inputs, default     Outputs, default     Outputs, default     Outputs, default     Default addresses of the integrated channels     — Digital inputs     — Digital outputs     66     Subprocess images     • Number of subprocess images, max.      Inputs     — of which central     Outputs     — of which central     Inputs	Process image	
	● Inputs, adjustable	2 048 byte
Outputs, default  Default addresses of the integrated channels  — Digital inputs — Digital outputs  66  Subprocess images  • Number of subprocess images, max.  1  Digital channels  • Inputs — of which central  • Outputs  — of which central  • Inputs — of which central  • Inputs — of which central  • Outputs — of which central  • Inputs — of which central  • Outputs  • Outputs — Of which central  • Outputs  • Outputs  • Outputs	<ul> <li>Outputs, adjustable</li> </ul>	2 048 byte
Default addresses of the integrated channels  - Digital inputs 66 - Digital outputs 66  Subprocess images  • Number of subprocess images, max. 1  Digital channels  • Inputs 65 536 - of which central 512  • Outputs 65 536 - of which central 512  Analog channels  • Inputs 4 096 - of which central 64  • Outputs 4 096	<ul><li>Inputs, default</li></ul>	256 byte
Digital inputs 66 Digital outputs 66  Subprocess images  ■ Number of subprocess images, max. 1  Digital channels  ■ Inputs 65 536 of which central 512  ■ Outputs 65 536 of which central 512  Analog channels  ■ Inputs 4 096 of which central 64  ■ Outputs 4 096	<ul> <li>Outputs, default</li> </ul>	256 byte
- Digital outputs 66  Subprocess images  ● Number of subprocess images, max. 1  Digital channels  ● Inputs 65 536  - of which central 512  ● Outputs 65 536  - of which central 512  Analog channels  ● Inputs 4 096  - of which central 64  ● Outputs 4 096	Default addresses of the integrated channels	
Subprocess images       ● Number of subprocess images, max.       1         Digital channels       65 536         Inputs       65 536         Outputs       65 536         Outputs       65 536         Of which central       512         Analog channels       4 096         Inputs       4 096         Outputs       64         Outputs       4 096	— Digital inputs	66
<ul> <li>Number of subprocess images, max.</li> <li>Digital channels         <ul> <li>Inputs</li> <li>Of which central</li> <li>Outputs</li> <li>Of which central</li> </ul> </li> <li>Analog channels         <ul> <li>Inputs</li> <li>Of which central</li> </ul> </li> <li>Analog channels         <ul> <li>Outputs</li> <li>4 096</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> <li>Outputs</li> </ul> </li> </ul>	— Digital outputs	66
Digital channels         ● Inputs       65 536         — of which central       512         ● Outputs       65 536         — of which central       512         Analog channels         ● Inputs       4 096         — of which central       64         ● Outputs       4 096	Subprocess images	
<ul> <li>Inputs         — of which central         — Outputs         — of which central         —</li></ul>	Number of subprocess images, max.	1
— of which central       512         ● Outputs       65 536         — of which central       512         Analog channels         ● Inputs       4 096         — of which central       64         ● Outputs       4 096	Digital channels	
◆ Outputs       65 536         — of which central       512         Analog channels         ◆ Inputs       4 096         — of which central       64         ◆ Outputs       4 096	• Inputs	65 536
— of which central       512         Analog channels       4 096         — of which central       64         ● Outputs       4 096	— of which central	512
Analog channels  ● Inputs	Outputs	65 536
● Inputs       4 096         — of which central       64         ● Outputs       4 096	— of which central	512
<ul> <li>— of which central</li> <li>Outputs</li> <li>64</li> <li>4 096</li> </ul>	Analog channels	
• Outputs 4 096	• Inputs	4 096
3.47	— of which central	64
— of which central 64	<ul><li>Outputs</li></ul>	4 096
	— of which central	64

Hardware configuration	
Number of expansion units, max.	0
Number of DP masters	
• integrated	2; 1 DP and 1 DP (drive)
• via CP	2; for DP
Number of operable FMs and CPs (recommended)	
• FM	8
• CP, PtP	8
• CP, LAN	10
Rack	
● Racks, max.	1
<ul> <li>Modules per rack, max.</li> </ul>	8
Time of day	
Clock	
<ul><li>Hardware clock (real-time)</li></ul>	Yes
<ul> <li>retentive and synchronizable</li> </ul>	Yes
Backup time	6 wk; At 40 °C ambient temperature
<ul> <li>Deviation per day, max.</li> </ul>	10 s
Operating hours counter	
• Number	4
<ul> <li>Number/Number range</li> </ul>	0 to 3
<ul> <li>Range of values</li> </ul>	0 to 2^31 hours (when using SFC 101)
Granularity	1 hour
• retentive	Yes; Must be restarted at each restart
Clock synchronization	
• supported	Yes
• to MPI, master	Yes
● to MPI, slave	Yes
• to DP, master	Yes
• to DP, slave	Yes
• in AS, master	Yes
• in AS, slave	Yes
Digital inputs	
Number of digital inputs	4
<ul> <li>of which inputs usable for technological</li> </ul>	4
functions	
Input characteristic curve in accordance with IEC	Yes
61131, type 1	
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	4

— up to 60 °C, max.	4
vertical installation	
— up to 40 °C, max.	4
Input voltage	
Rated value (DC)	24 V
● for signal "0"	-3 to +5V
• for signal "1"	+15 to +30V
Input current	
• for signal "1", typ.	7 mA
Input delay (for rated value of input voltage)	
for counter/technological functions	
— at "0" to "1", max.	10 μs; Typical
— at "1" to "0", max.	10 μs; Typical
Cable length	1 7 31
• shielded, max.	1 000 m
Chiologa, max.	
Digital outputs	
Number of digital outputs	8
of which high-speed outputs	8
Functions	For technology functions, e.g. high-speed cam switch signals
Short-circuit protection	Yes
Response threshold, typ.	1 A
Limitation of inductive shutdown voltage to	48 V
Controlling a digital input	No
Switching capacity of the outputs	
• on lamp load, max.	5 W
Load resistance range	
• lower limit	48 Ω
• upper limit	4 kΩ
Output voltage	
• for signal "0", max.	3 V; (2L+)
● for signal "1", min.	Rated voltage -2.5 V
Output current	
● for signal "1" rated value	0.5 A
• for signal "1" permissible range for 0 to 60 °C,	5 mA
min.	
<ul> <li>for signal "1" permissible range for 0 to 60 °C, max.</li> </ul>	0.6 A
• for signal "0" residual current, max.	0.3 mA
Parallel switching of two outputs	
• for uprating	No
<ul> <li>for redundant control of a load</li> </ul>	No
Switching frequency	

• with registive lead may	100 Hz
with resistive load, max.	0.2 Hz; According to IEC 60947-5-1, DC-13
with inductive load, max.	100 Hz
• on lamp load, max.	100 ΠΖ
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	4 A
— up to 60 °C, max.	3 A
all other mounting positions	
— up to 40 °C, max.	3 A
Cable length	
• shielded, max.	1 000 m
Analog inputs	
Number of analog inputs	0
Analog outputs	
Number of analog outputs	0
Encoder	
Connectable encoders	
• 2-wire sensor	No
Interfaces	
Number of industrial Ethernet interfaces	0
Number of RS 485 interfaces	2
Number of RS 422 interfaces	0
1. Interface	
Interface type	Integrated RS 485 interface
Physics	RS 485
Isolated	Yes
Power supply to interface (15 to 30 V DC), max.	200 mA
Functionality	
• MPI	Yes
<ul> <li>PROFIBUS DP master</li> </ul>	Yes
<ul> <li>PROFIBUS DP slave</li> </ul>	Yes
Point-to-point connection	No
MPI	
Number of connections	32
Transmission rate, max.	12 Mbit/s
Services	
— PG/OP communication	Yes
— Routing	Yes
Global data communication	Yes
— S7 basic communication	Yes
or basic communication	

— S7 communication	Yes
<ul> <li>— S7 communication, as client</li> </ul>	No; but via CP and loadable FB
— S7 communication, as server	Yes; Connection configured on one side only
DP master	
Transmission rate, max.	12 Mbit/s
<ul> <li>Number of DP slaves, max.</li> </ul>	124
Services	
— PG/OP communication	Yes
— Routing	Yes
<ul> <li>Global data communication</li> </ul>	No
<ul> <li>— S7 basic communication</li> </ul>	Yes; I blocks only
— S7 communication	Yes
<ul> <li>S7 communication, as client</li> </ul>	No; but via CP and loadable FB
<ul> <li>S7 communication, as server</li> </ul>	Yes; Connection configured on one side only
— Equidistance	Yes
— Isochronous mode	Yes; OB 61
— SYNC/FREEZE	Yes
<ul> <li>Activation/deactivation of DP slaves</li> </ul>	Yes
— DPV1	Yes
Address area	
— Inputs, max.	8 192 byte
— Outputs, max.	8 192 byte
User data per DP slave	
— Inputs, max.	244 byte
— Outputs, max.	244 byte
DP slave	
<ul><li>Transmission rate, max.</li></ul>	12 Mbit/s
automatic baud rate search	No
<ul> <li>Address area, max.</li> </ul>	32
<ul> <li>User data per address area, max.</li> </ul>	32 byte
Services	
— PG/OP communication	Yes
— Routing	Yes; Only with active interface
<ul> <li>Global data communication</li> </ul>	No
— S7 basic communication	No
— S7 communication	Yes
— S7 communication, as client	No; but via CP and loadable FB
— S7 communication, as server	Yes; Connection configured on one side only
<ul> <li>— Direct data exchange (slave-to-slave communication)</li> </ul>	Yes
— DPV1	No

Transfer memory	
— Inputs	244 byte
— Outputs	244 byte
2. Interface Interface type	Integrated RS 485 interface
Physics	RS 485
Isolated	Yes
Power supply to interface (15 to 30 V DC), max.	200 mA
Functionality	200 (111)
• MPI	No
PROFIBUS DP master	Yes; DP(DRIVE)-Master
PROFIBUS DP slave	No
Point-to-point connection	No
DP master	
Transmission rate, max.	12 Mbit/s
Number of DP slaves, max.	64
Services	
— PG/OP communication	No
— Routing	No
Global data communication	No
S7 basic communication	No
— S7 communication	No
— S7 communication  — Equidistance	Yes
Lyuluistance      Isochronous mode	Yes
	No
— SYNC/FREEZE	Yes
Activation/deactivation of DP slaves	
— DPV1	No
Address area	4 004 b. to
— Inputs, max.	1 024 byte
— Outputs, max.	1 024 byte
User data per DP slave	0441-4-
— Inputs, max.	244 byte
— Outputs, max.	244 byte
DP slave	http://www.ad.siemens.de/support in Product Support area
• GSD file	
Transmission rate, max.	12 Mbit/s
Communication functions	
PG/OP communication	Yes
Global data communication	
• supported	Yes
<ul><li>Number of GD loops, max.</li></ul>	8

<ul><li>Number of GD packets, max.</li></ul>	8
<ul> <li>Number of GD packets, transmitter, max.</li> </ul>	8
<ul> <li>Number of GD packets, receiver, max.</li> </ul>	8
<ul><li>Size of GD packets, max.</li></ul>	22 byte
<ul> <li>Size of GD packet (of which consistent), max.</li> </ul>	22 byte
S7 basic communication	
• supported	Yes
<ul> <li>User data per job, max.</li> </ul>	76 byte
<ul> <li>User data per job (of which consistent), max.</li> </ul>	76 byte; 76 bytes (with X_SEND or X_RCV), 76 bytes (with X_PUT or X_GET as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
User data per job, max.	See online help of STEP 7 (shared parameters of the SFBs/FBs and of the SFCs/FCs of S7 Communication)
S5 compatible communication	
• supported	Yes; via CP and loadable FC
Number of connections	
• overall	32
<ul> <li>usable for PG communication</li> </ul>	31
<ul> <li>reserved for PG communication</li> </ul>	1
<ul> <li>adjustable for PG communication, min.</li> </ul>	1
<ul> <li>adjustable for PG communication, max.</li> </ul>	31
<ul> <li>usable for OP communication</li> </ul>	31
<ul> <li>reserved for OP communication</li> </ul>	1
<ul> <li>adjustable for OP communication, min.</li> </ul>	1
<ul> <li>adjustable for OP communication, max.</li> </ul>	31
<ul><li>usable for S7 basic communication</li></ul>	30
<ul> <li>reserved for S7 basic communication</li> </ul>	0
<ul> <li>adjustable for S7 basic communication, min.</li> </ul>	0
<ul> <li>adjustable for S7 basic communication,</li> </ul>	30
max.	
usable for routing	8; additional
S7 message functions	
Number of login stations for message functions, max.	32; Depending on the configured connections for PG/OP and S7 basic communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	60
Test commissioning functions	
Status block	Yes

Single step	Yes
Number of breakpoints	2
Status/control	
Status/control variable	Yes
Variables	Inputs, outputs, memory bits, DB, times, counters
<ul><li>Number of variables, max.</li></ul>	30
<ul><li>of which status variables, max.</li></ul>	30
<ul><li>of which control variables, max.</li></ul>	14
Forcing	
<ul><li>Forcing</li></ul>	Yes
<ul><li>Forcing, variables</li></ul>	Inputs, outputs
<ul> <li>Number of variables, max.</li> </ul>	10
Diagnostic buffer	
• present	Yes
<ul><li>Number of entries, max.</li></ul>	100
— adjustable	No
Interrupts/diagnostics/status information	
Alarms	No
Diagnostic functions	No
Diagnostics indication LED	
<ul> <li>Status indicator digital input (green)</li> </ul>	Yes
<ul> <li>Status indicator digital output (green)</li> </ul>	Yes
Potential separation	
Potential separation digital inputs	
• between the channels and backplane bus	Yes
Potential separation digital outputs	
• between the channels and backplane bus	Yes
Permissible potential difference	
between different circuits	75 V DC/60 V AC
Isolation	
Isolation tested with	500 V DC
Ambient conditions	
Ambient conditions  Ambient temperature during operation	
• min.	0 °C
• max.	60 °C
Confirmation	
Configuration Configuration software	
STEP 7	Yes
Programming	
Command set	see instruction list
- Command 36t	

Nesting levels	8
<ul><li>System functions (SFC)</li></ul>	see instruction list
<ul> <li>System function blocks (SFB)</li> </ul>	see instruction list
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
— SCL	Yes
— CFC	Yes
— GRAPH	Yes
— HiGraph®	Yes
Know-how protection	
User program protection/password protection	Yes
Cycle time monitoring	
• lower limit	1 ms
• upper limit	6 000 ms
• adjustable	Yes
• preset	150 ms
Dimensions	
Width	160 mm
Height	125 mm
Depth	130 mm
Weights	
Weight, approx.	750 g
last modified:	03/23/2017